

ABSTRACT OF THE DISCLOSURE

Apparatus and Method to Enable Transparent Memory Hot Plug/Remove

5 In some embodiments, an apparatus includes a processor, an expander memory bridge location, a memory coupled to the expander memory bridge location, and a bus controller including intercept logic to intercept and block communication from the processor to the expander memory bridge location and to emulate an expander memory bridge. In some embodiments, a method includes intercepting and
10 blocking a status request to a device, regardless of whether the device is installed, and responding to the status request.

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